

## 700V N-Channel POWER MOSFET

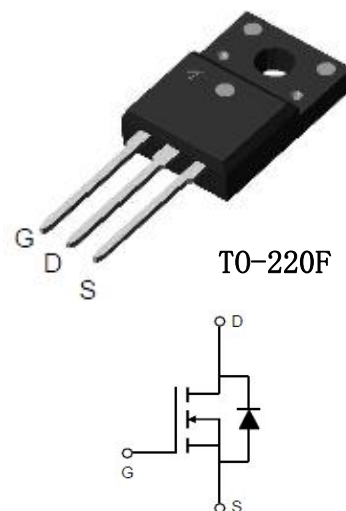
### Features

- $V_{DSS}=700V$   $I_D=13A$
- $R_{DS(ON)}=0.78\Omega(\text{Max.})@V_{GS}=10V$
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- Low ON Resistance

### Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

### PIN DESCRIPTION



Part Number	Package	Marking	ROHS Status	Packing
SI13N70F	T0-220F	SI13N70F	Pb-Free	Box (Tube)

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Unit
$V_{DSS}$	Drain-Source Voltage	700	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Continuous Drain Current	13	A
$I_{DM}$	Pulsed Drain Current	52	A
$P_D$	Power Dissipation ( $T_C=25^\circ\text{C}$ )	70	W
$I_{AR}$	Avalanche Current	16	A
$E_{AS}$	Single Pulse Avalanche Energy	1280	mJ
$E_{AR}$	Repetitive Avalanche Energy	1238	mJ
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, Junction – Case.	$R_{thJC}$	1.78	$^\circ\text{C}/\text{W}$
Thermal resistance, Junction – Ambient.	$R_{thJA}$	62.5	

## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	TYP	Max	Unit
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-source breakdown voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	700	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V	-	-	1	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±30V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate-Source Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	3.0	-	4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =6.5A	-	-	0.78	Ω
<b>Dynamic Characteristic</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz	-	1540	-	pF
C <sub>oss</sub>	Output Capacitance		-	175	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	21	-	
Q <sub>G</sub>	Gate Total Charge	V <sub>DS</sub> =560V, I <sub>D</sub> =13A, V <sub>GS</sub> =10V,	-	45	-	nC
Q <sub>gs</sub>	Gate-Source charge		-	8.6	-	
Q <sub>gd</sub>	Gate-Drain charge		-	21	-	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>DD</sub> =350V, I <sub>D</sub> =13A, R <sub>G</sub> =25Ω	-	30	-	nS
t <sub>r</sub>	Rise time		-	15	-	
t <sub>d(off)</sub>	Turn-off delay time		-	94	-	
t <sub>f</sub>	Fall time		-	22	-	
<b>Drain-Source Body Diode Characteristics</b>						
V <sub>SD</sub>	Body Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>SD</sub> =6.5A	-	-	1.4	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =13A, dI <sub>F</sub> /dt =100A/μs	-	380	-	nS
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	4.5	-	uC
I <sub>S</sub>	Continuous Drain-Source Diode Forward Current		-	-	13	A
I <sub>SM</sub>	Pulsed Drain-Source Diode Forward Current		-	-	52	A

### Note:

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.L=10.0mH, V<sub>DD</sub>=50V, R<sub>G</sub>= 25 Ω, Starting T<sub>J</sub>= 25 °
- 3.Pulse Test: Pulse width ≤ 350μs, Duty Cycle ≤ 1%

## ■ Switching Time Test Circuit and Wave forms

Figure A: Gate Charge Test Circuit and Waveform

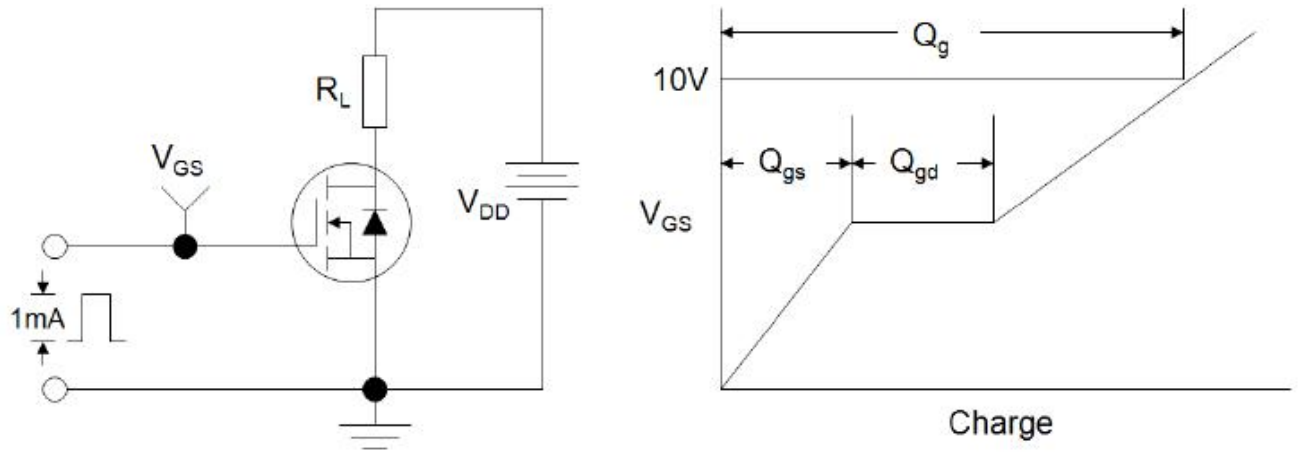


Figure B: Resistive Switching Test Circuit and Waveform

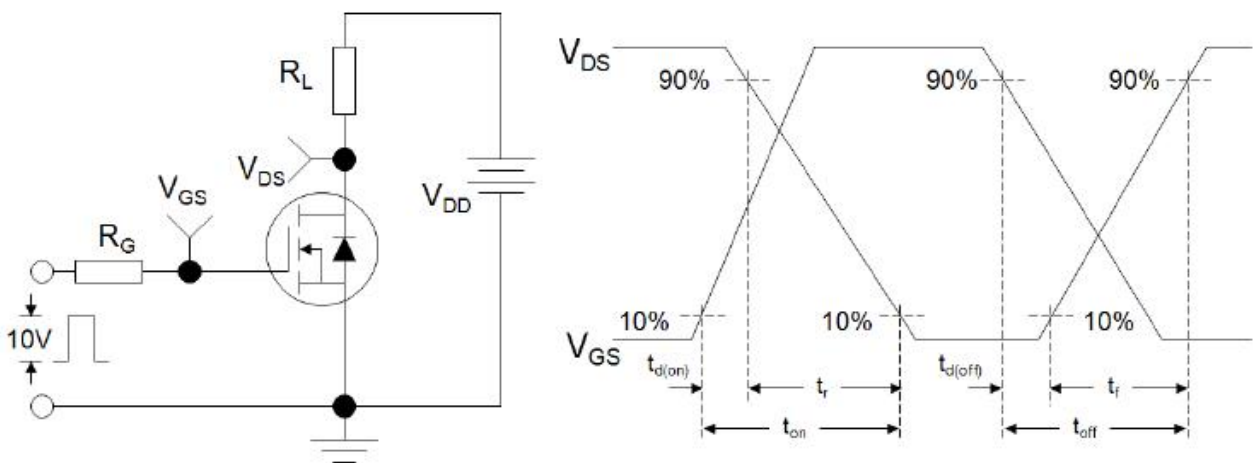
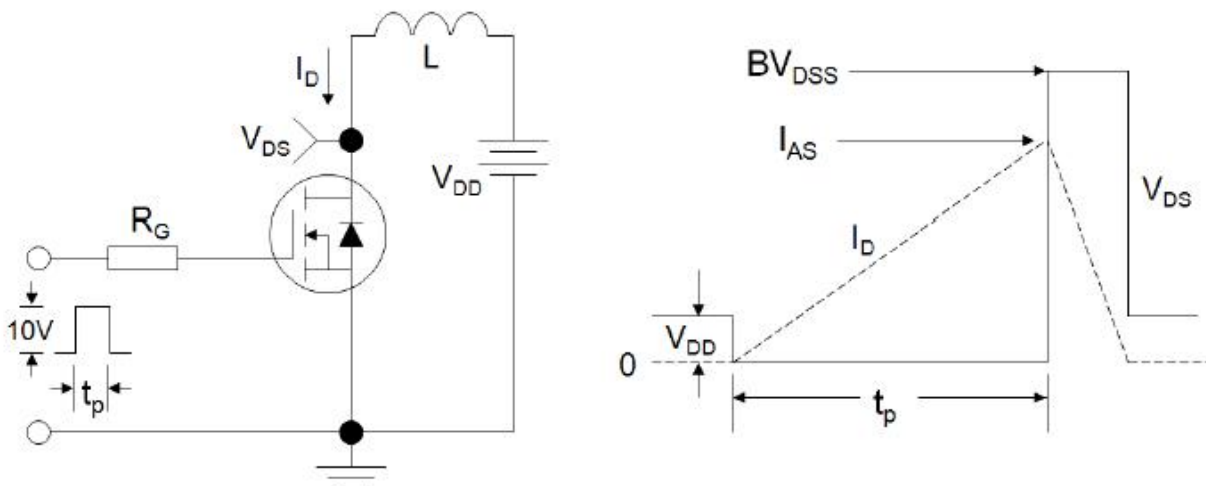


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



## Typical Performance Characteristics

Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )

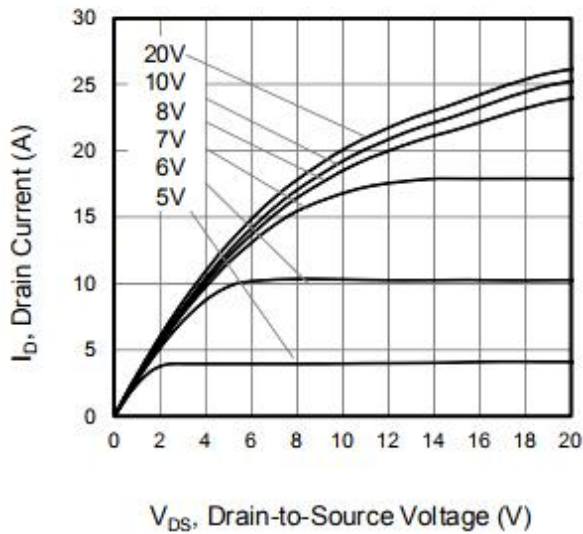


Figure 2. On-Resistance vs. Drain Current

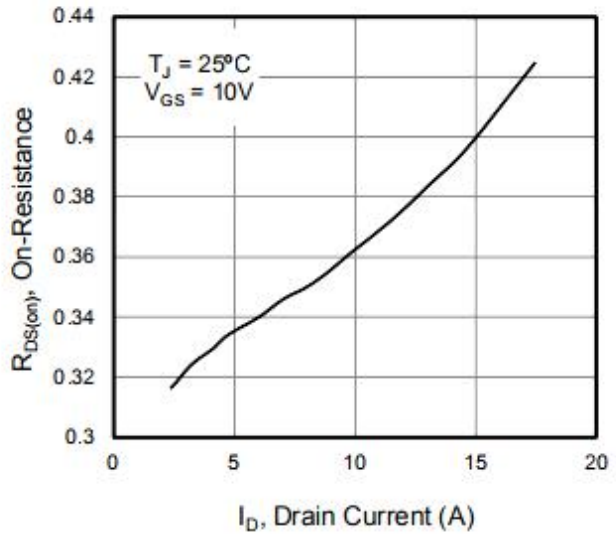


Figure 3.  $BV_{DSS}$  vs. Temperature

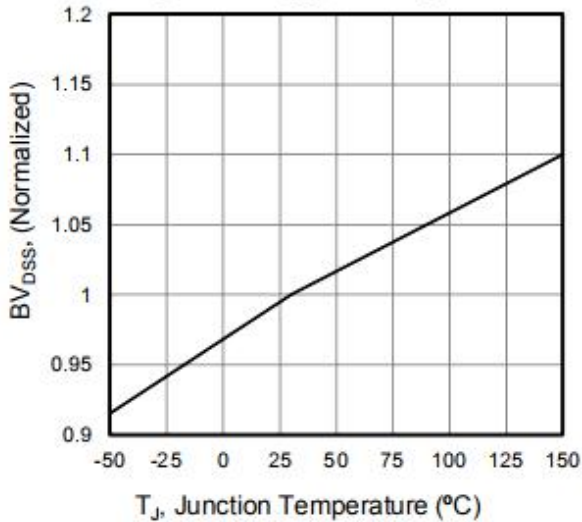


Figure 4. On-Resistance vs. Temperature

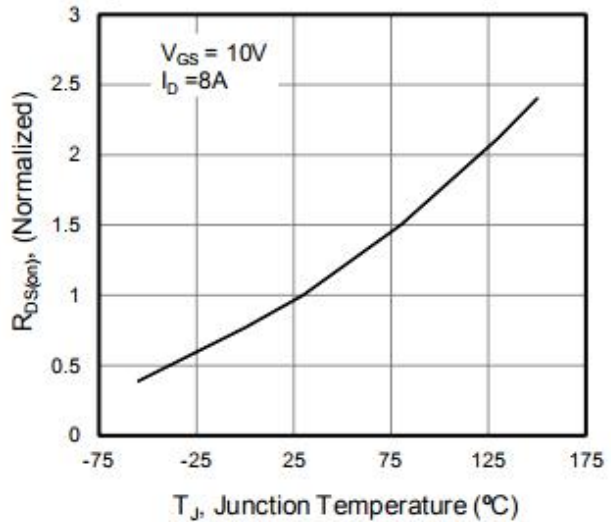


Figure 5. Gate Charge

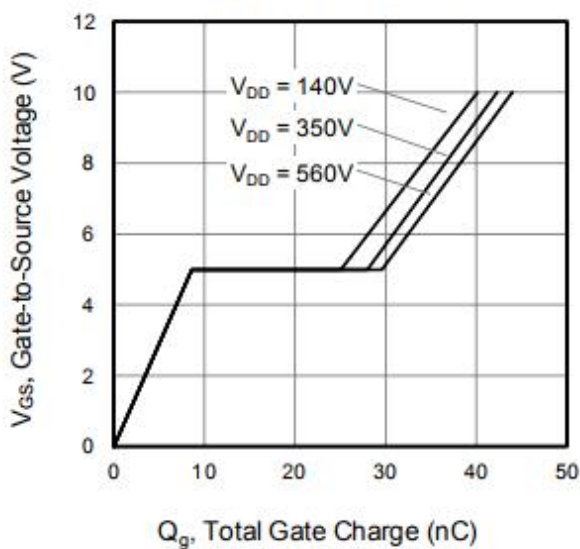
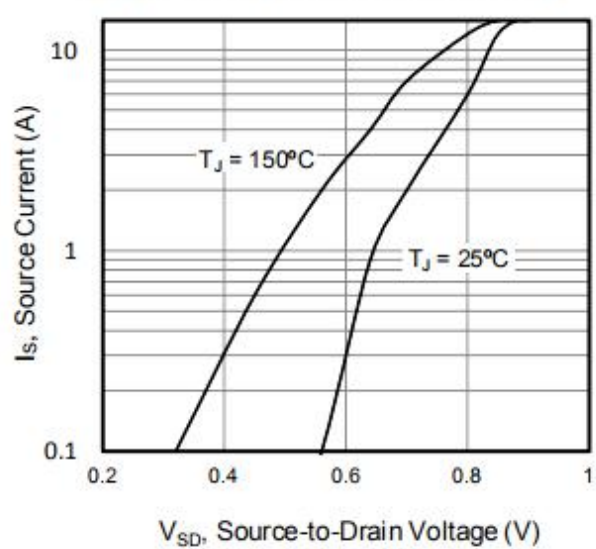
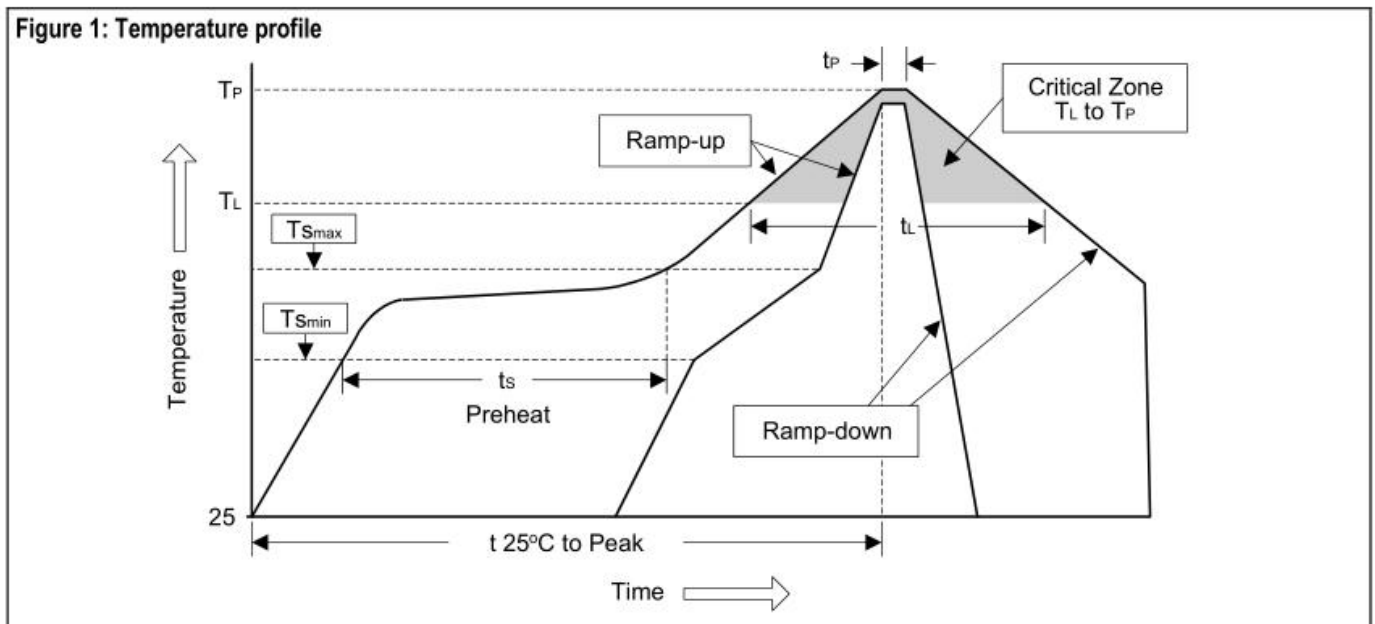


Figure 6. Body Diode Forward Voltage



## Soldering Methods for Products

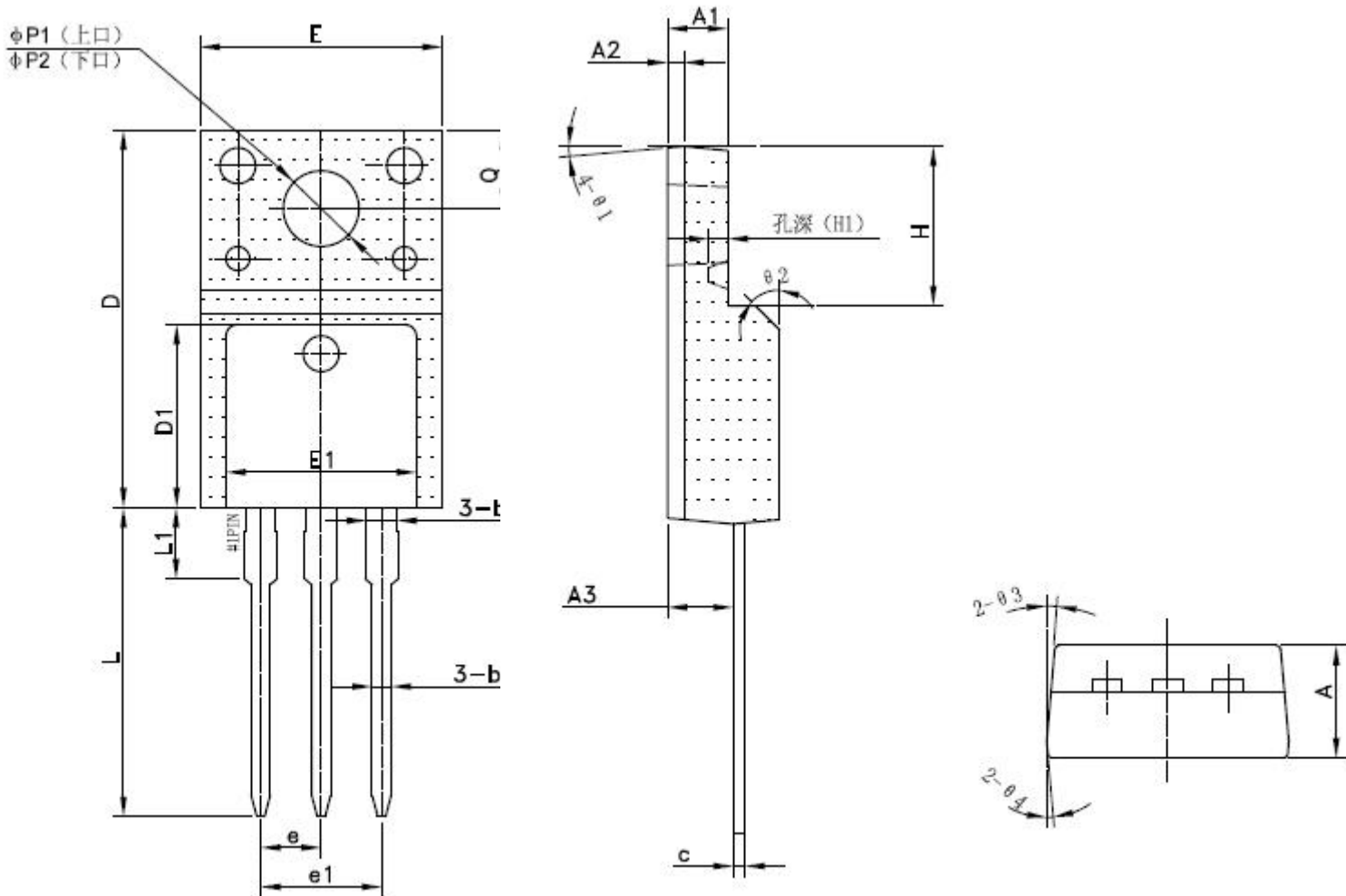
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate(TL to TP)	<3°C/sec	<3°C/sec
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(min to max)(ts)	60 to 120 sec	60 to 180 sec
Tsmax to TL		
- ramp-up rate	<3°C/sec	<3°C/sec
Time maintained above:		
-Temperature(TL)	183°C	217°C
-Time(tL)	60 to 150 sec	60 to 150 sec
Peak Temperature(Tp)	240°C+0/-5°C	250°C+0/-5°C
Time within 5°C of actual Peak Temperature	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25 °C to Peak Temperature	<6 minutes	<8 minutes



**Note** :1.Storage environment: Temperature=10°C to 35@Humidity=45%±15%

- 2.Reflow soldering of surface-mount devices
- 3.Flow(wave) soldering(solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices	245°C±5°C	5sec±1sec
Pb-free devices	250°C+0/-5°C	5sec±1sec

**Package Outline**


Millimeter(mm)					
Symbol	Min	Max	Symbol	Min	Max
A	4.50	4.90	E	9.96	10.36
A1	2.44	2.64	E1	8.00TYP	
A2	0.60	0.80	e	2.54TYP	
A3	2.56	2.96	e1	5.08TYP	
b	0.70	0.95	H	6.50	6.90
b1	1.28TYP		L	12.48	13.20
c	0.45	0.65	L1	2.93TYP	
D	15.67	16.07	P1	2.98	3.38
D1	7.70TYP		P2	3.20.	3.60

**■ Important Notice**

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Any semiconductor under specific conditions are possible to certain failure or malfunction rate ; Customers are responsible in the use of Si-Trend products to system design and manufacturing in compliance with safety standards and adopting safety measures, To avoid the potential risk of failure may cause the personal safety and property loss。

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**■ Modify record**

Date	Version	Description	Pagination
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